

FIG. 1

FIG.2

ADDRESS[7:0]	DATA[63:0]
11	00-CF
13	D0
	PORT 0 TRANSMIT DESCRIPTOR
	D1
	PORT 1 TRANSMIT DESCRIPTOR
	D2
	PORT 2 TRANSMIT DESCRIPTOR
	D3
	PORT 3 TRANSMIT DESCRIPTOR
	D4
	PORT 4 TRANSMIT DESCRIPTOR
	D5
	PORT 5 TRANSMIT DESCRIPTOR
	D6
	PORT 6 TRANSMIT DESCRIPTOR
	D7
	PORT 7 TRANSMIT DESCRIPTOR
15	D8
	PORT 8 TRANSMIT DESCRIPTOR
17	D9-DF
	UNUSED
	E0-FF
	32 ARL TABLE ENTRIES

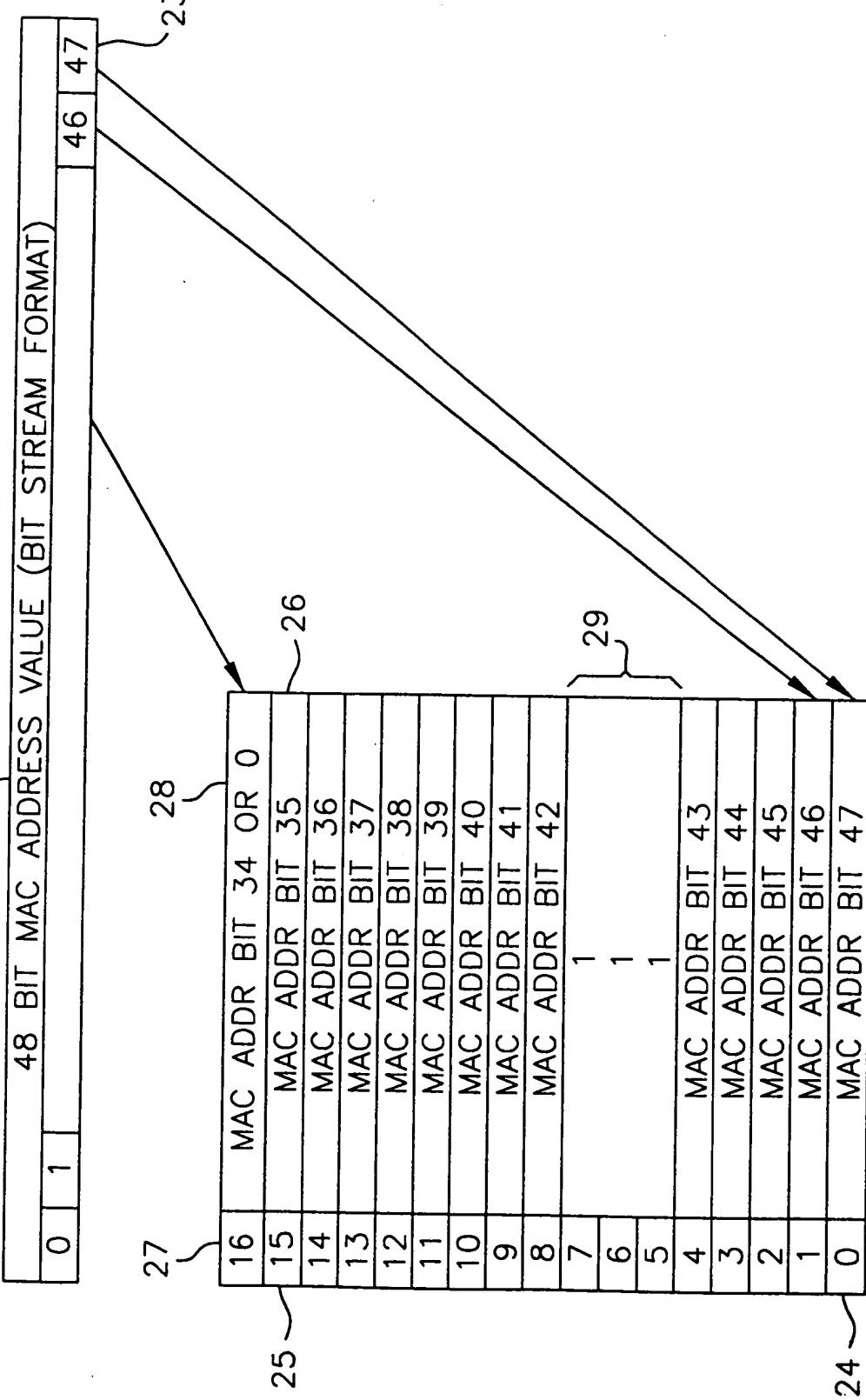
FIG.3

ADDRESS[16:0]	DATA[63:0]
19	0000-00FF
	MEMORY BLOCK 0
	0000-01FF
	MEMORY BLOCK 1
	0000-02FF
	MEMORY BLOCK 2
	0000-FEFF
	MEMORY BLOCK 3 TO 2046
	0000-FFFF
	MEMORY BLOCK 2047
	0000-1FFFF
	OPTIONAL MEMORY BLOCK 2048-4095

FIG. 4

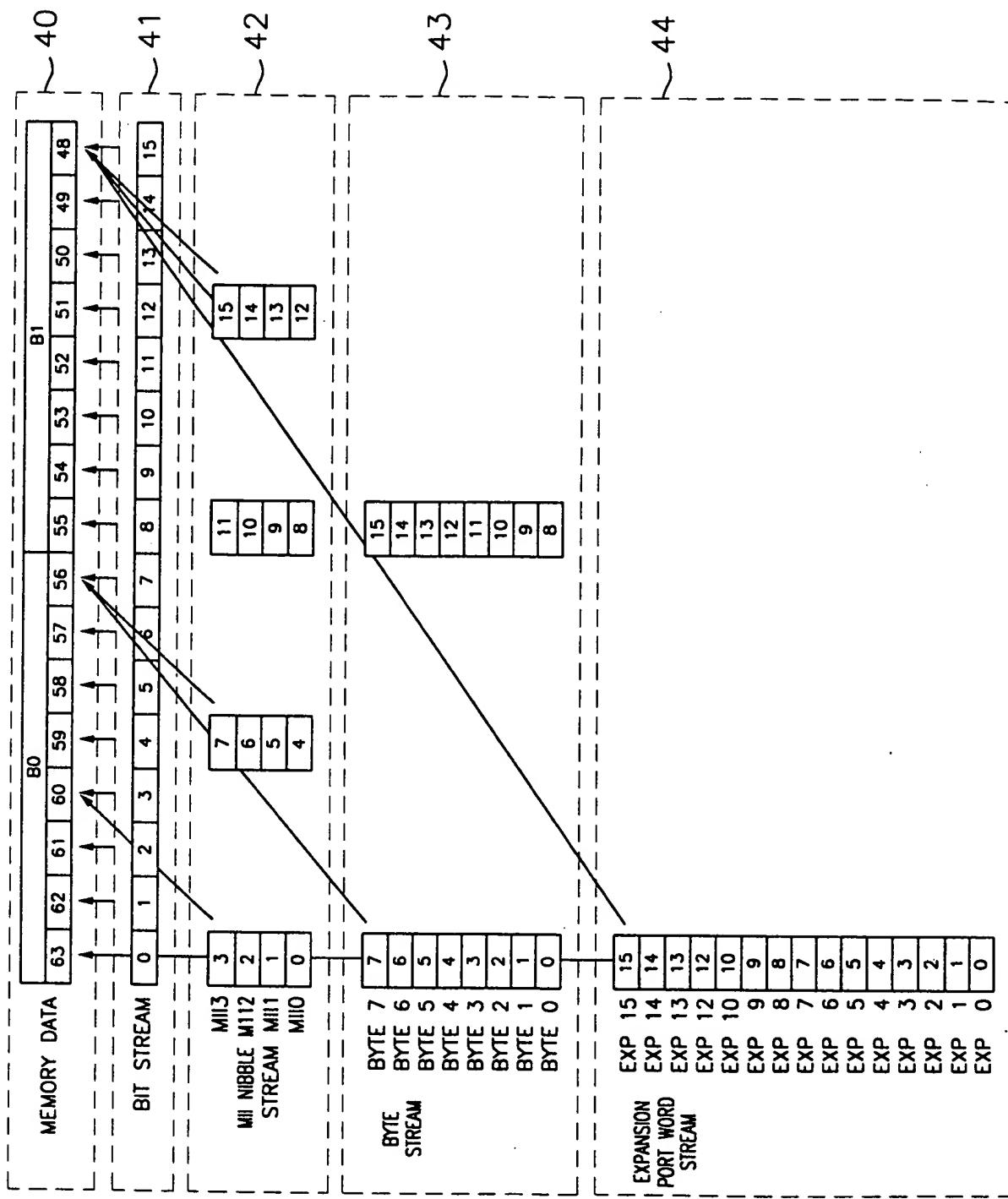
FIG.5

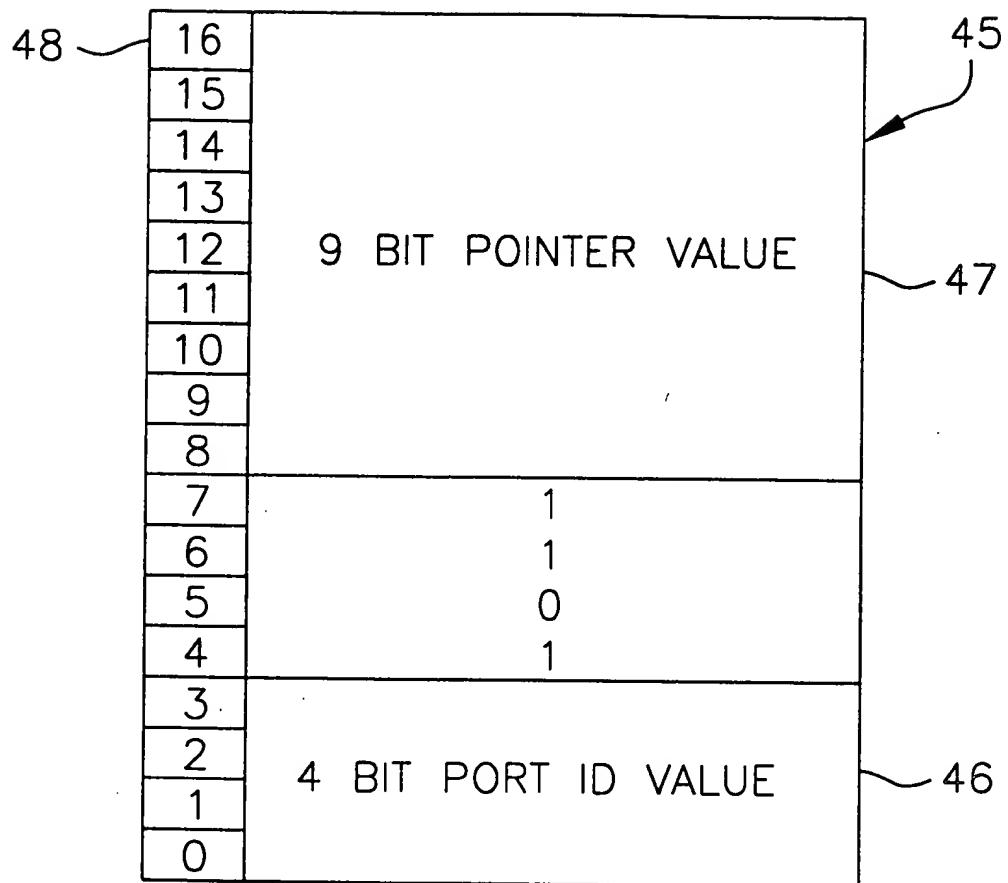
21



DATE 2026/08/00

FIG. 6

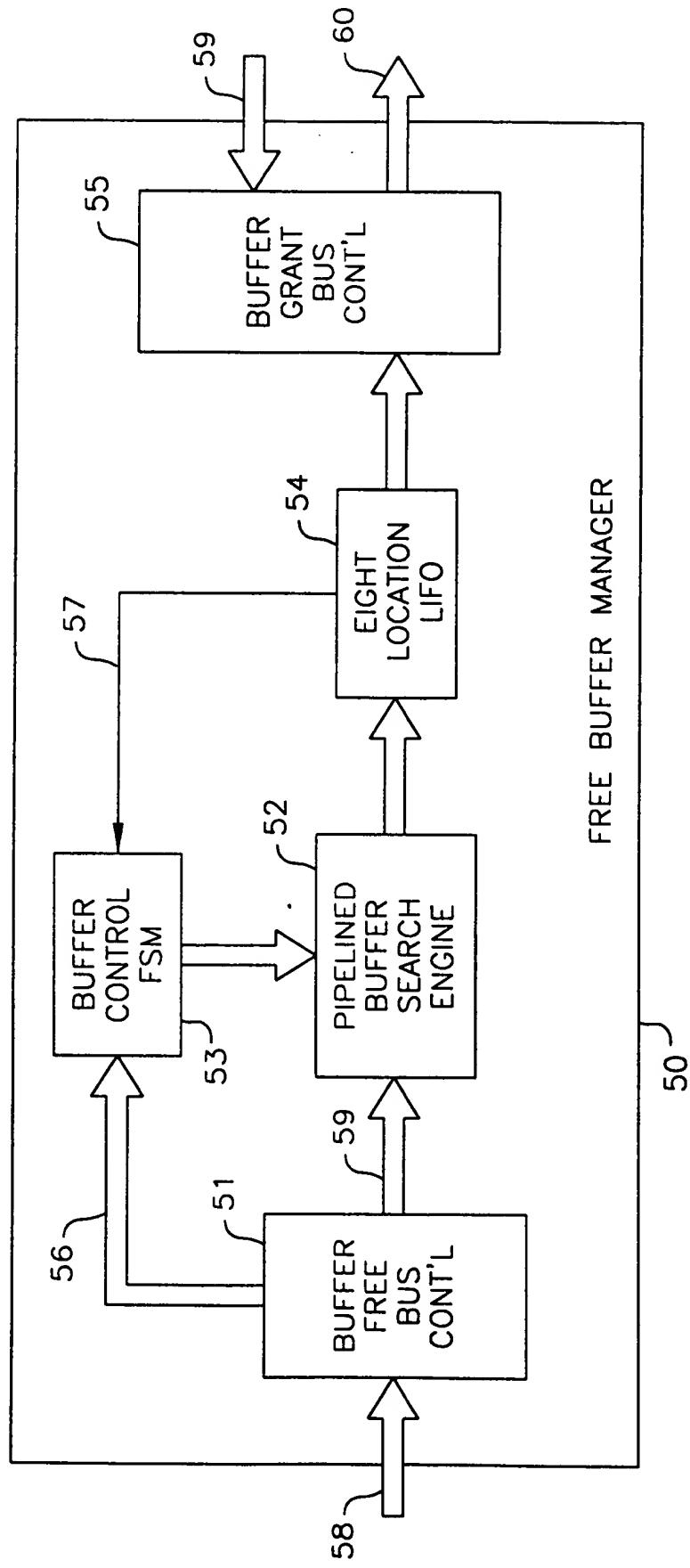




TRANSMIT DESCRIPTOR POINTER ADDRESS

FIG. 7

FIG. 8



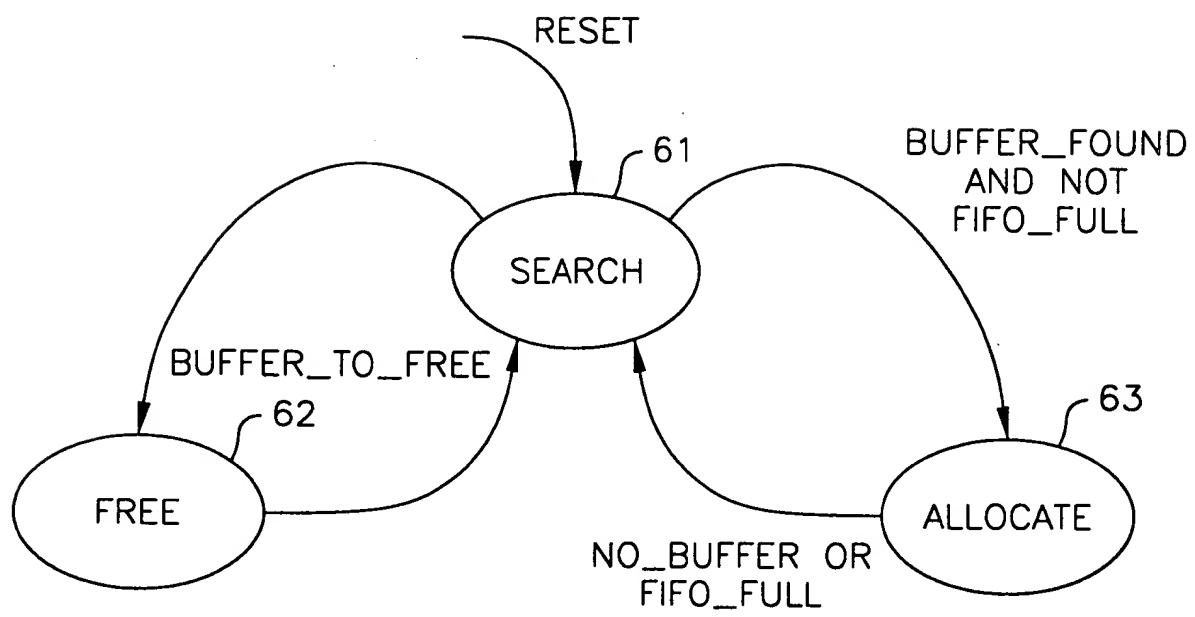
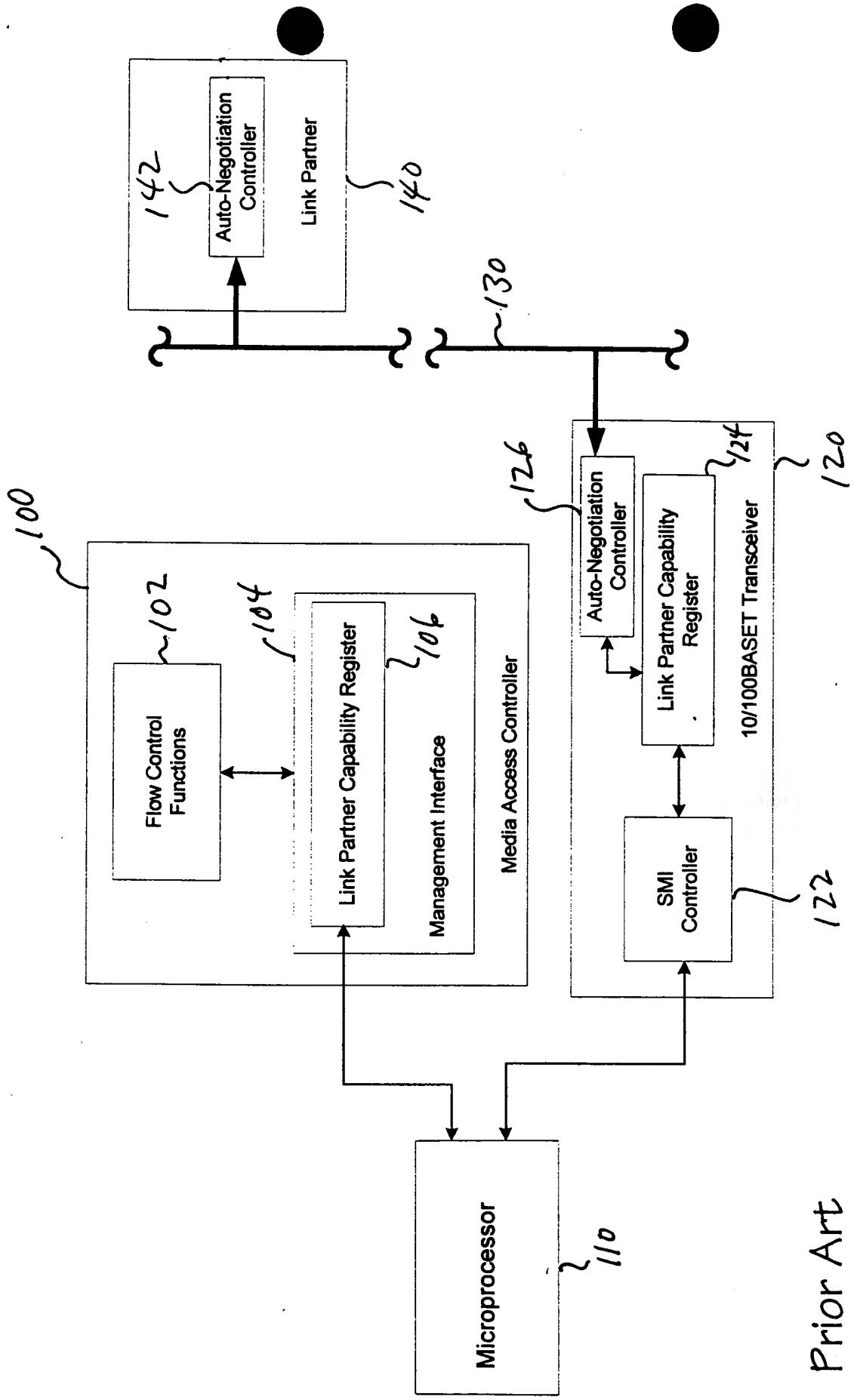
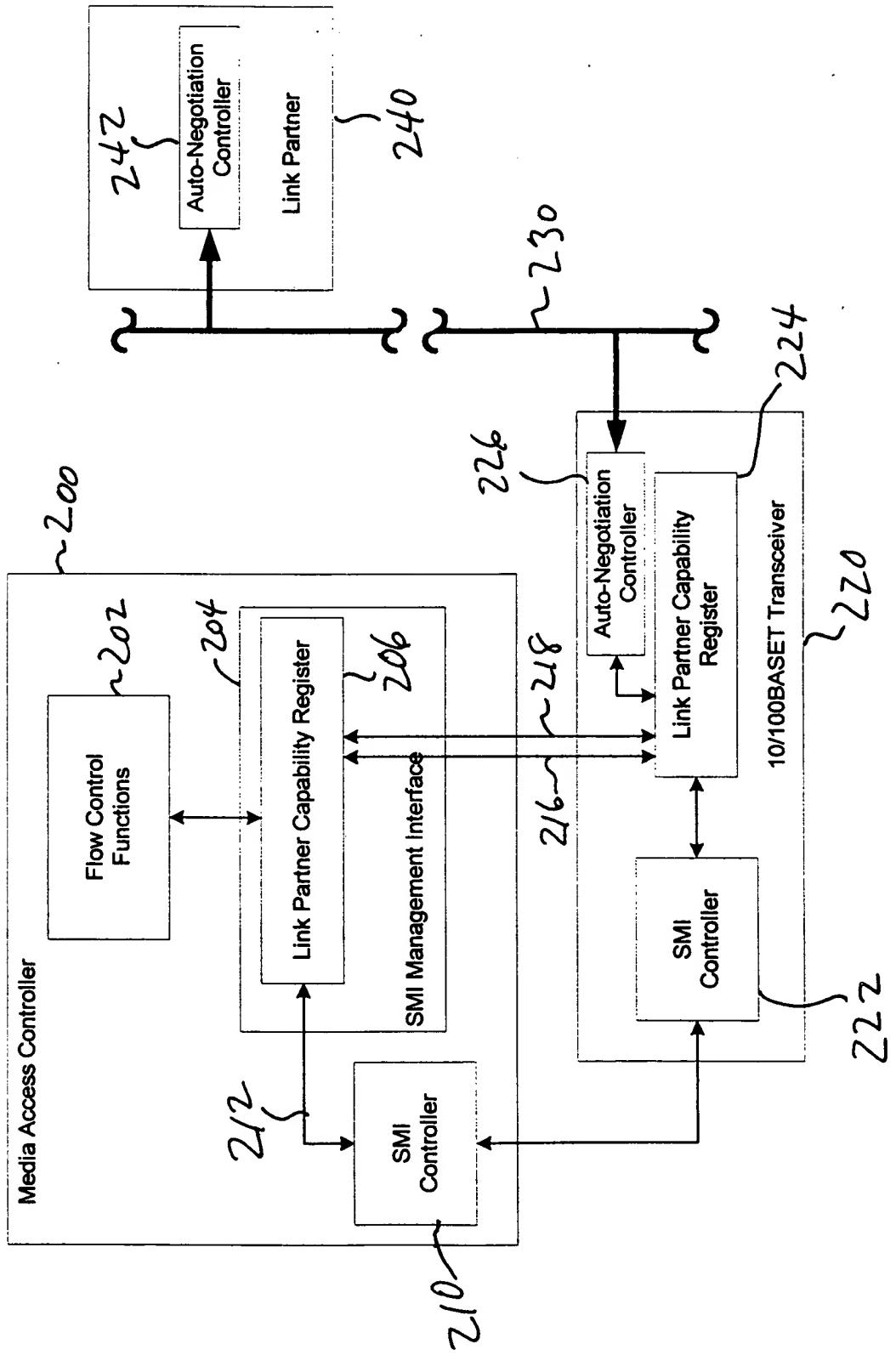


FIG.9



Prior Art

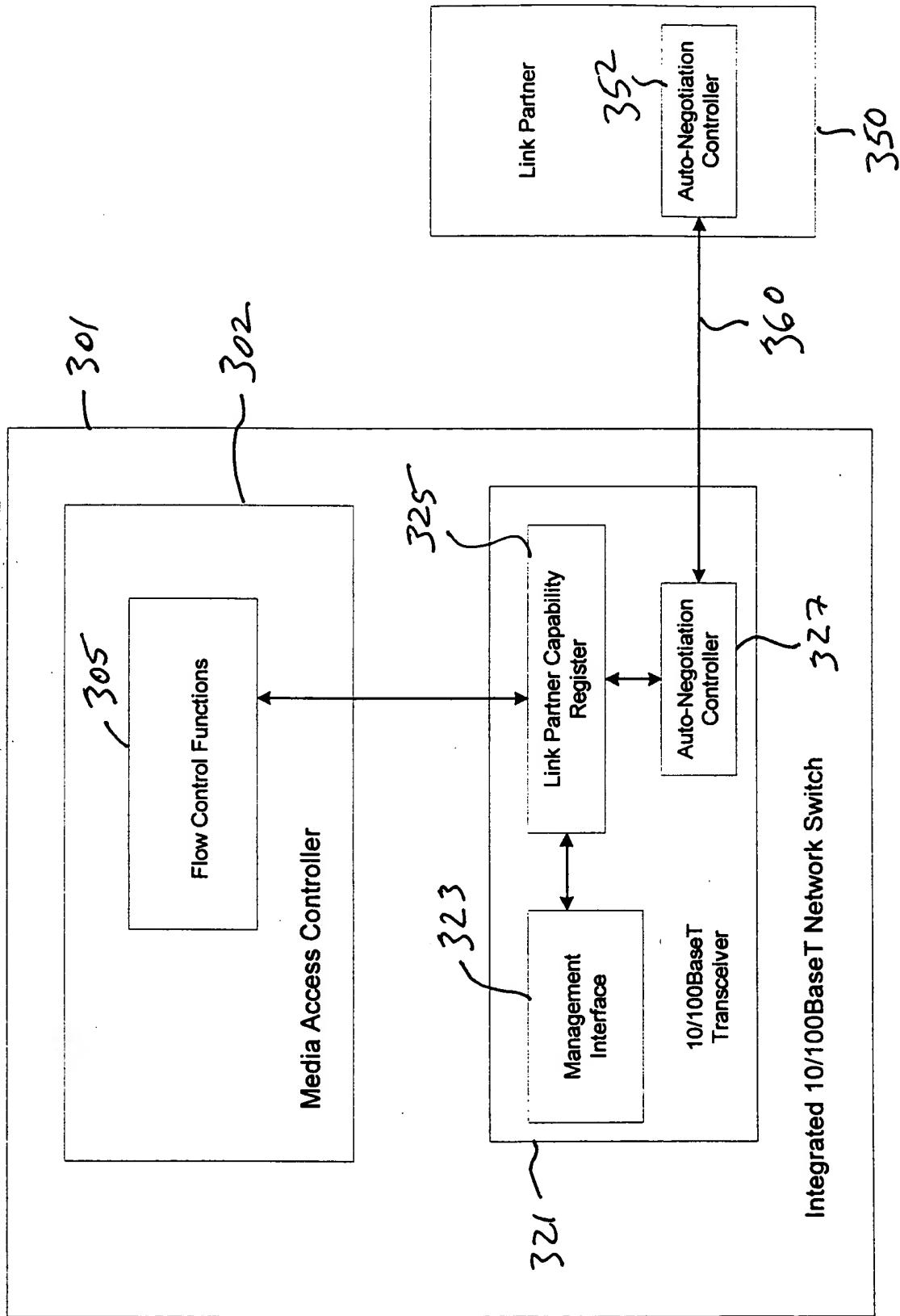
Figure 10



Prior Art

Figure 11

Figure 12



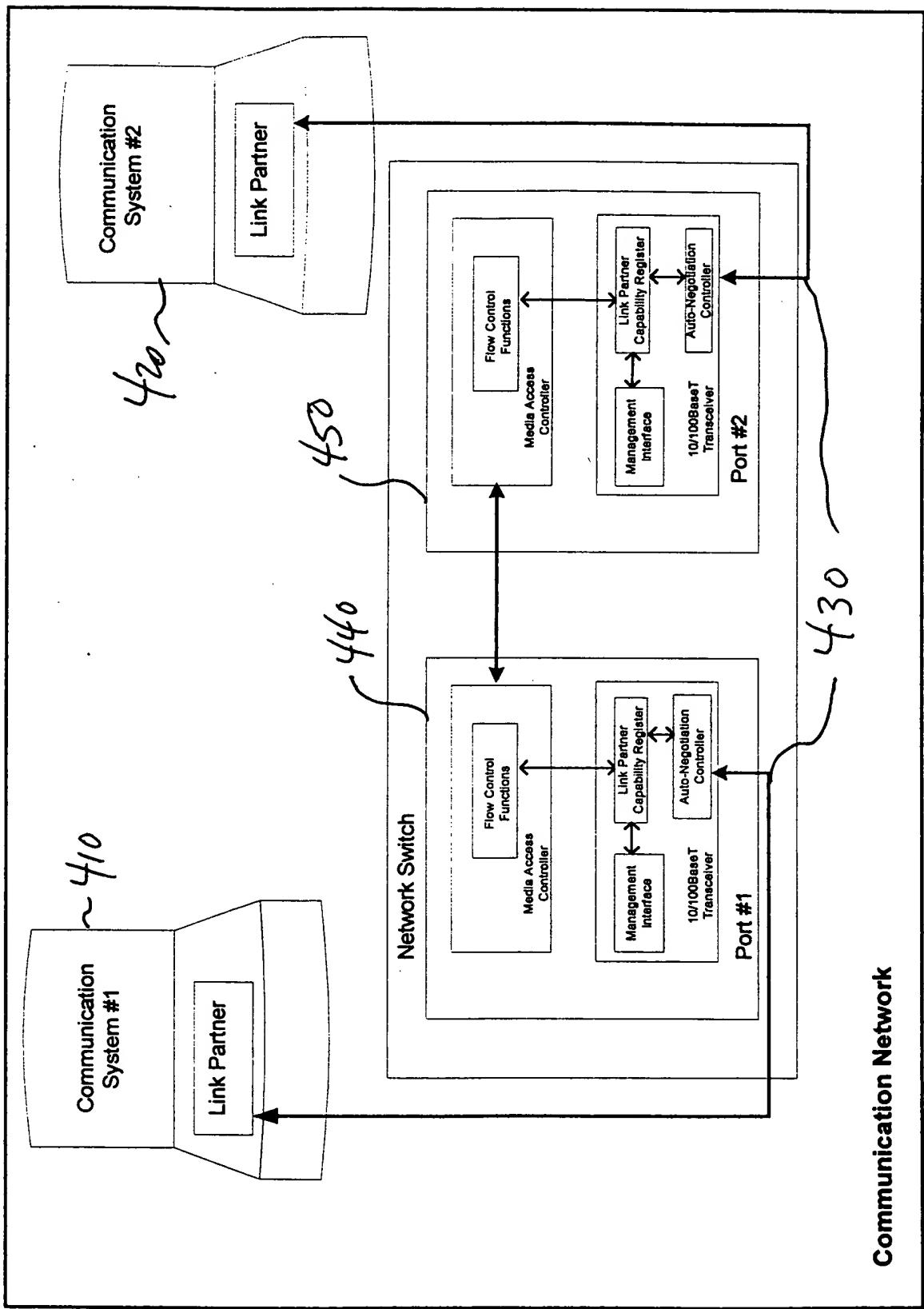


Figure 13

Communication Network